



SN: 10/028058 Agilent Case No.: 10010586-1
Inventor(s): Geefay, et al.
Title: Wafer Level Package With Silicon Gasket
Judy Liao Shie (408) 345-8920

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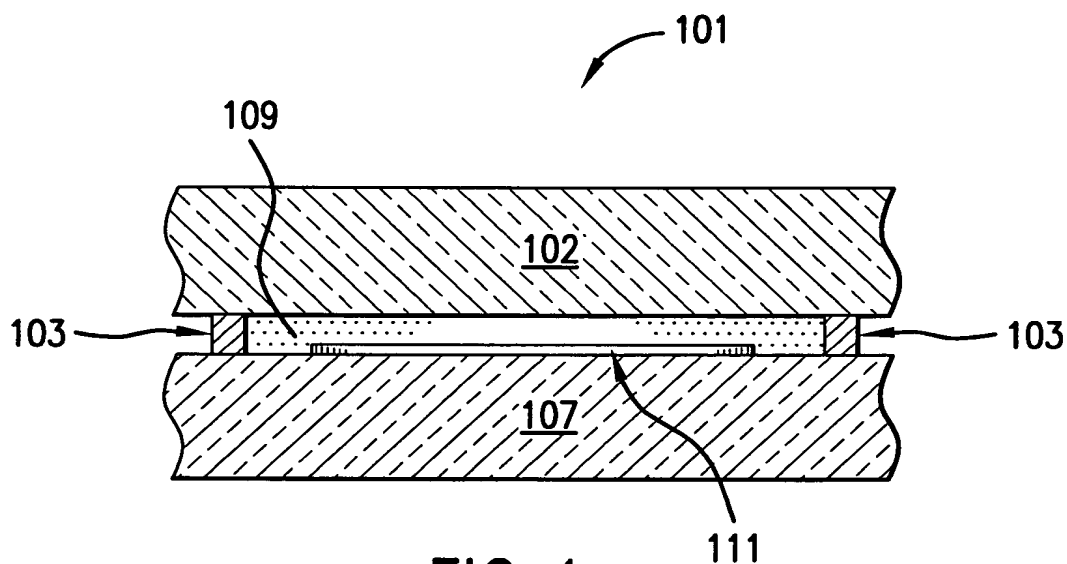


FIG. 1
PRIOR ART



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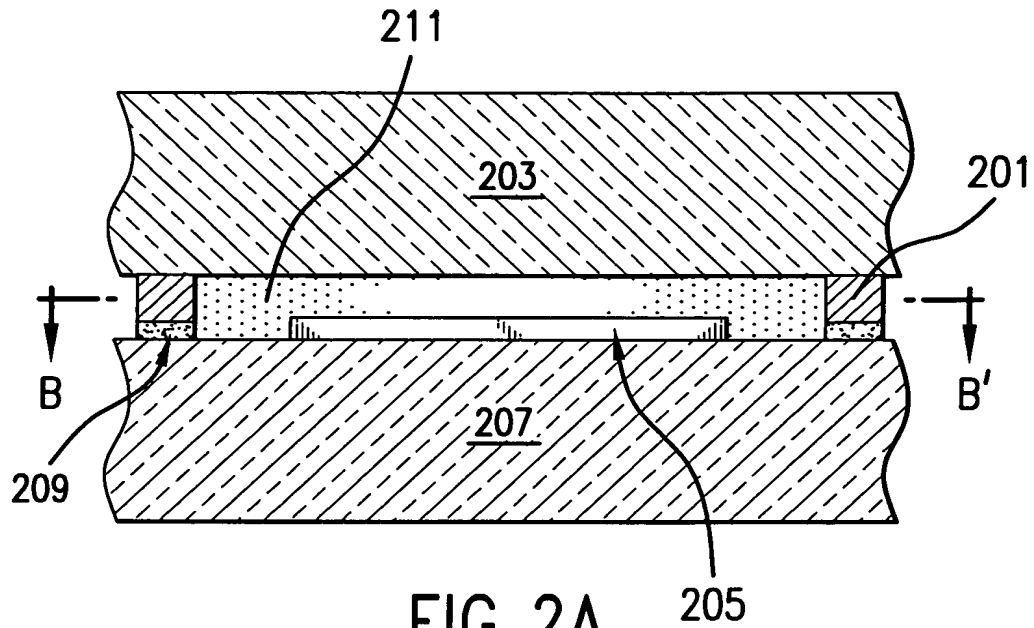


FIG. 2A

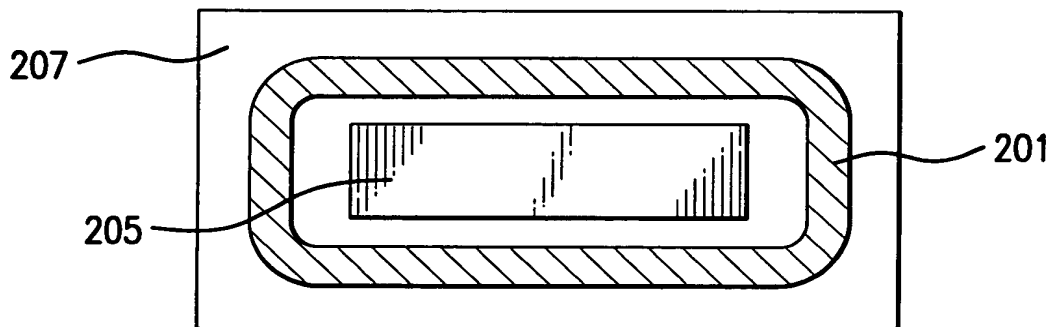


FIG. 2B



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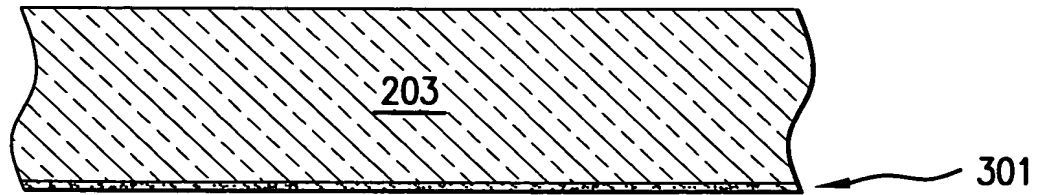


FIG. 3A

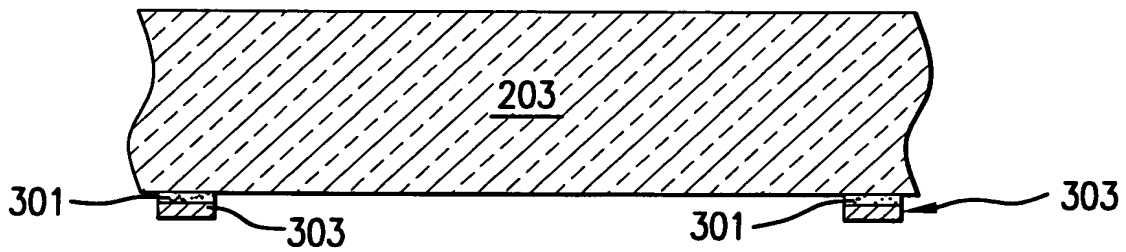


FIG. 3B

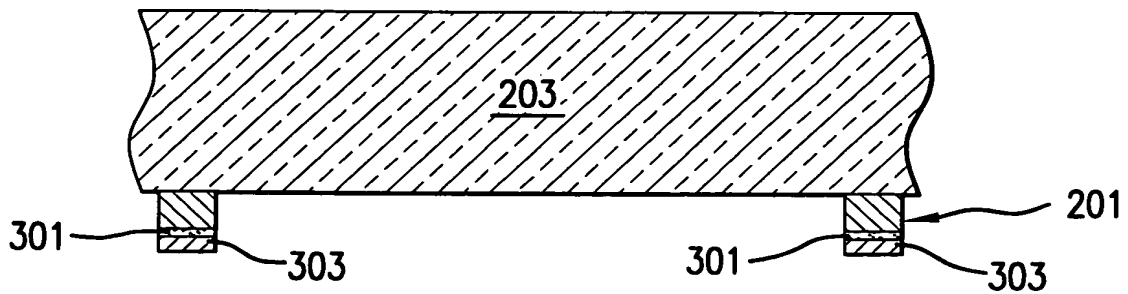


FIG. 3C

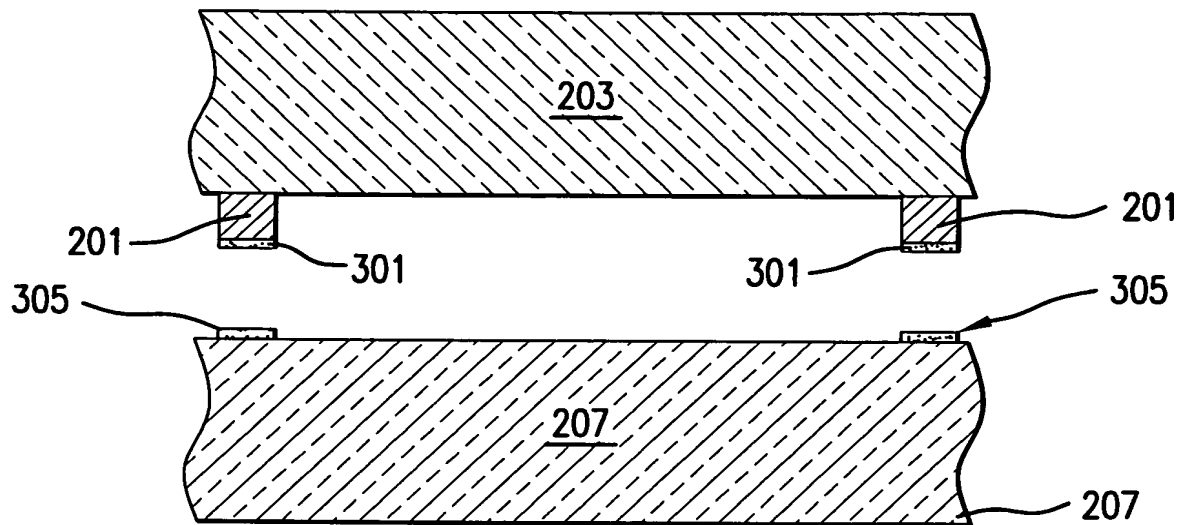


FIG. 3D

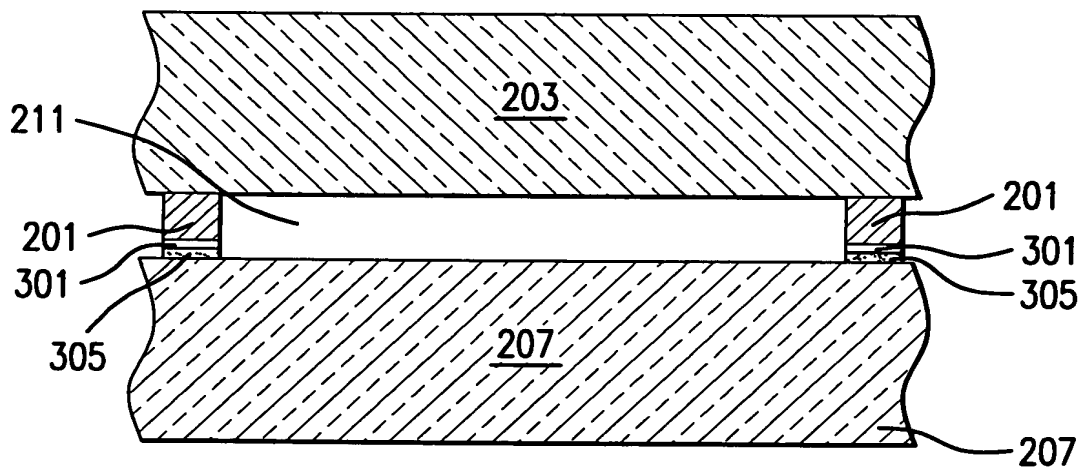
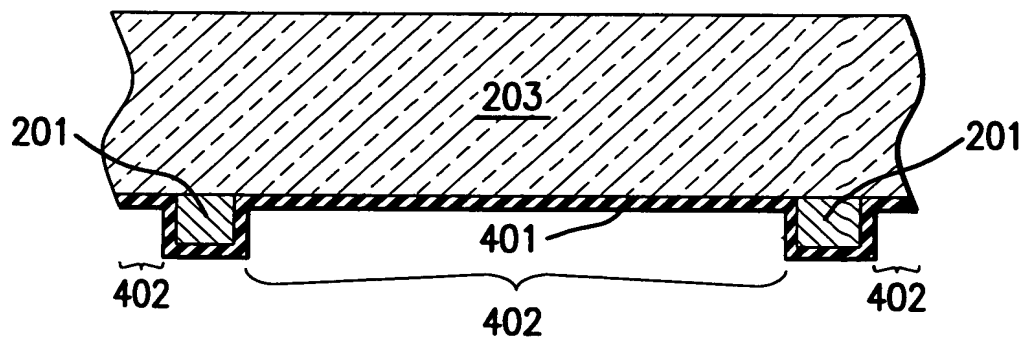
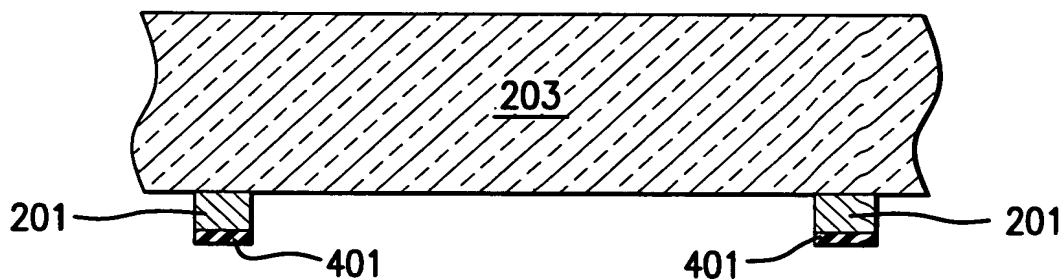
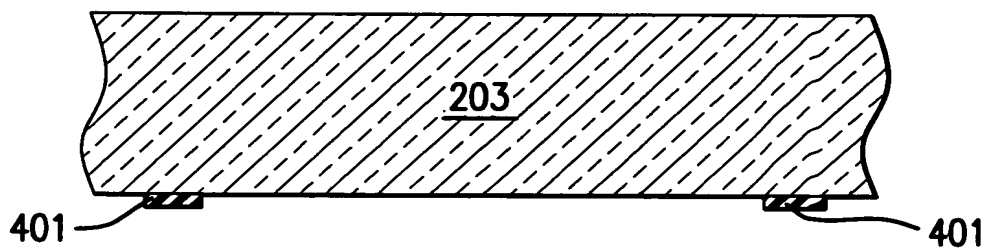
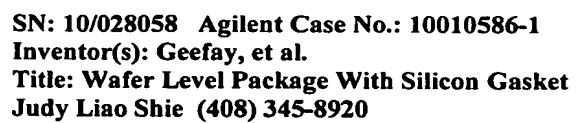


FIG. 3E



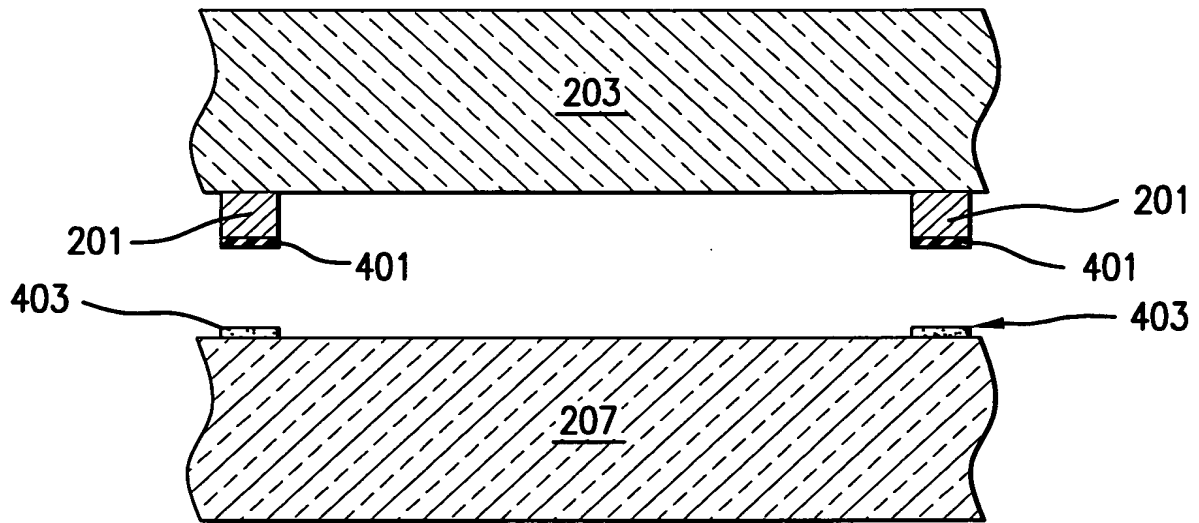


FIG. 4D

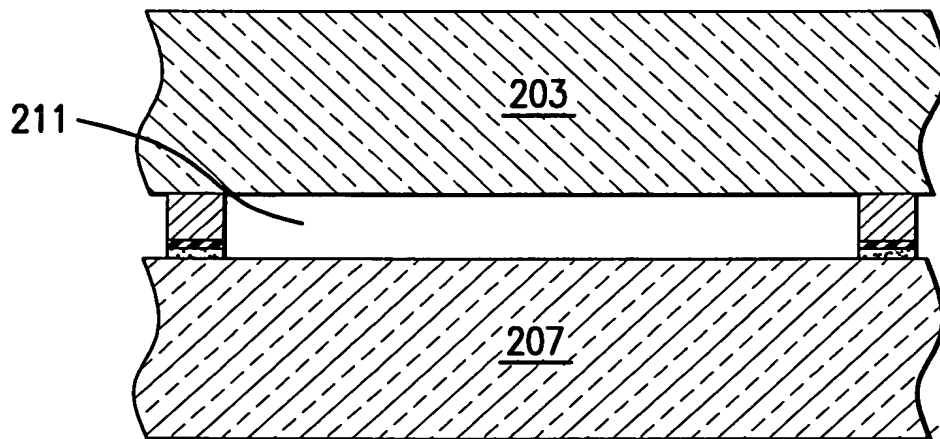


FIG. 4E



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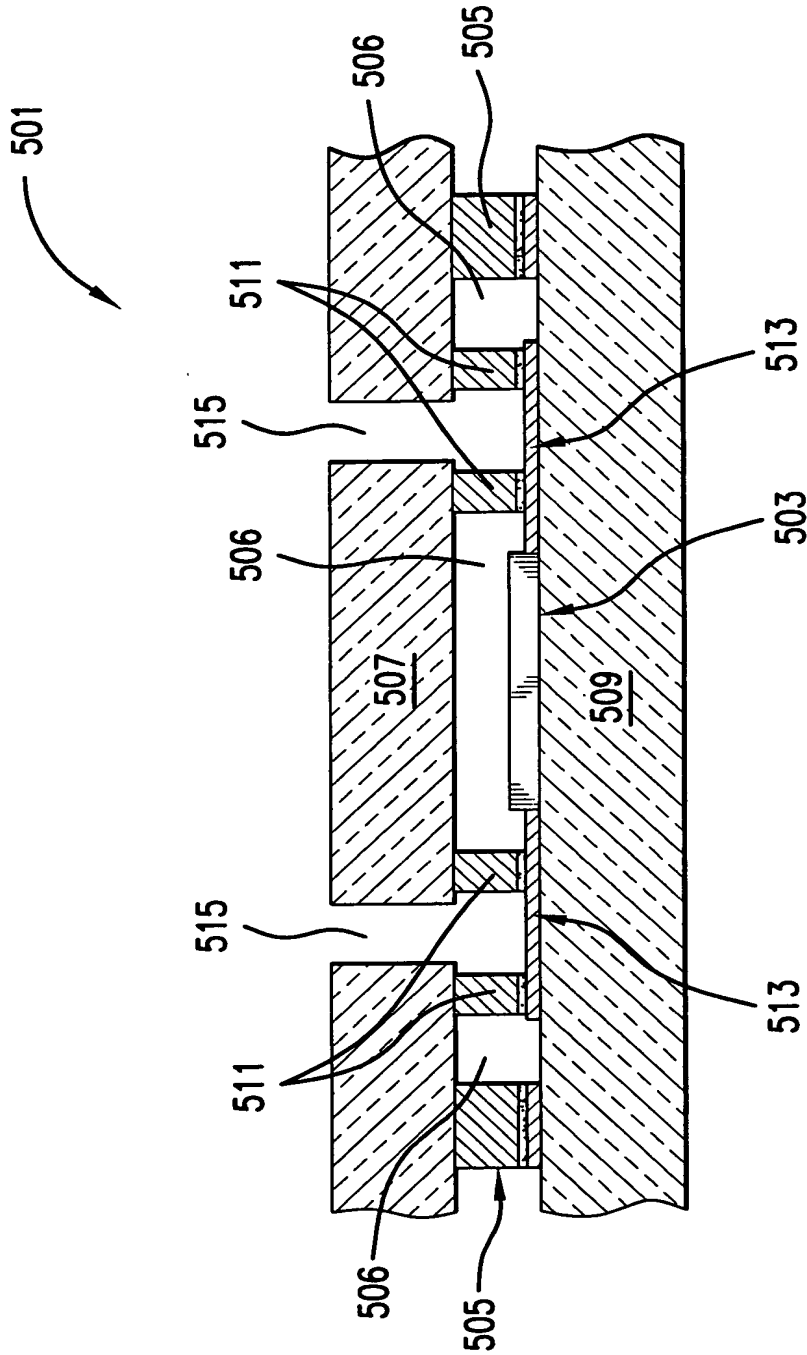


FIG. 5